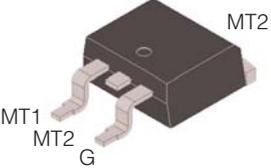
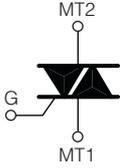


HIGH COMMUTATION TRIAC

<p>D²PAK</p>  	<p>On-State Current</p> <p>16 Amp</p>	<p>Gate Trigger Current</p> <p>≤ 50 mA</p>
	<p>Off-State Voltage</p> <p>200 V ÷ 800 V</p>	
<p>This series of TRIACs uses a high performance PNPN technology.</p> <p>These parts are intended for general purpose AC switching applications with highly inductive loads.</p>		

Absolute Maximum Ratings, according to IEC publication No. 134

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
$I_{T(RMS)}$	RMS On-state Current (full sine wave)	All Conduction Angle, $T_c = 95\text{ °C}$	16	A
I_{TSM}	Non-repetitive On-State Current	Full Cycle, 60 Hz ($t = 16.7\text{ ms}$)	176	A
I_{TSM}	Non-repetitive On-State Current	Full Cycle, 50 Hz ($t = 20\text{ ms}$)	160	A
I^2t	Fusing Current	$t_p = 10\text{ ms}$, Half Cycle	128	A ² s
I_{GM}	Peak Gate Current	$20\text{ }\mu\text{s max.}$ $T_j = 125\text{ °C}$	4	A
$P_{G(AV)}$	Average Gate Power Dissipation	$T_j = 125\text{ °C}$	1	W
di/dt	Critical rate of rise of on-state current	$I_G = 2 \times I_{GT}$, $t_r \leq 100\text{ ns}$ $f = 120\text{ Hz}$, $T_j = 125\text{ °C}$	50	A/ μs
T_j	Operating Temperature		(-40 +125)	°C
T_{stg}	Storage Temperature		(-40 +150)	°C
T_{sld}	Soldering Temperature	10s max	260	°C

SYMBOL	PARAMETER	VOLTAGE					Unit
		B	D	M	S	N	
V_{DRM}	Repetitive Peak Off State Voltage	200	400	600	700	800	V
V_{RRM}							

HIGH COMMUTATION TRIAC

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle)

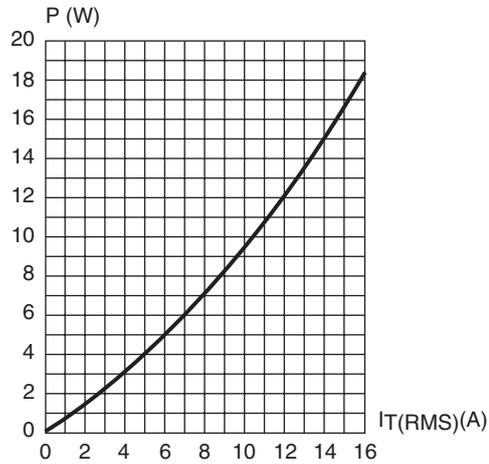


Fig. 2: RMS on-state current versus case temperature (full cycle).

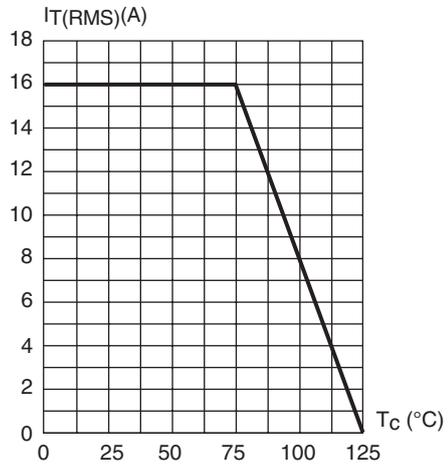


Fig. 3: Relative variation of thermal impedance versus pulse duration.

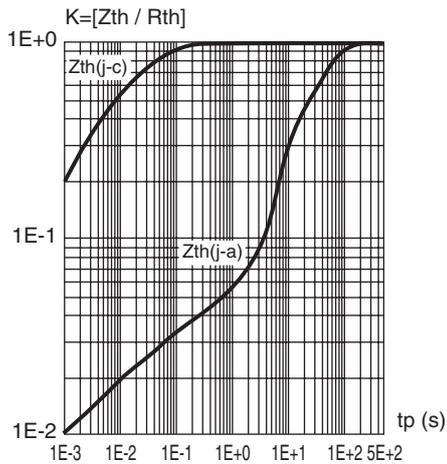


Fig. 4: On-state characteristics (maximum values)

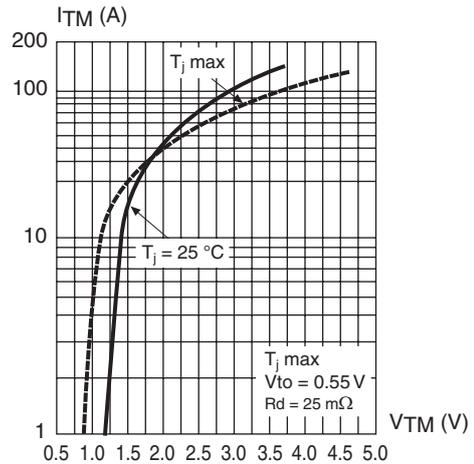


Fig. 5: Surge peak on-state current versus number of cycles

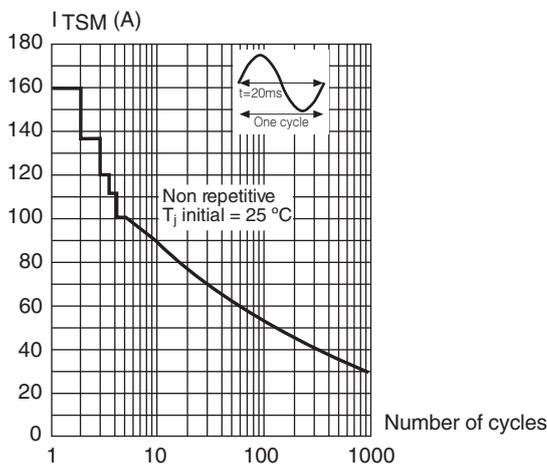
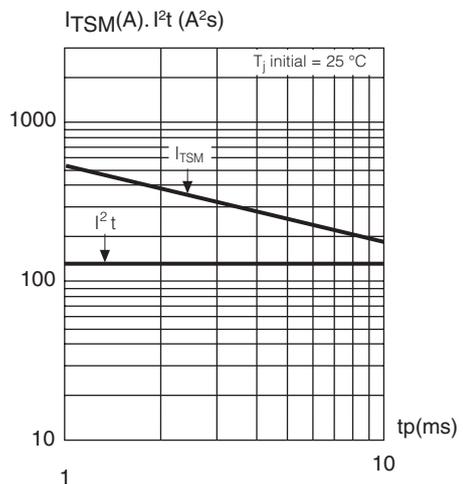


Fig. 6: Non repetitive surge peak on-state current for a sinusoidal pulse with width: $t_p < 10$ ms, and corresponding value of $I^2 t$.



HIGH COMMUTATION TRIAC

Fig. 7: Relative variation of gate trigger current, holding current and latching versus junction temperature (typical values)

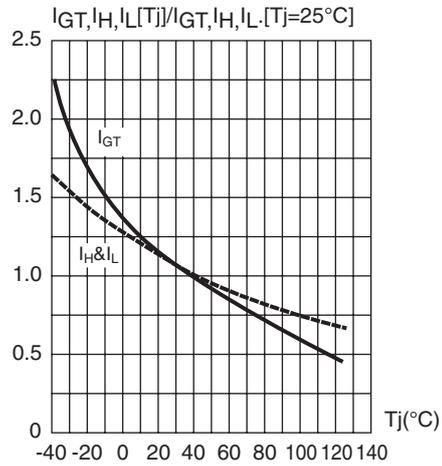
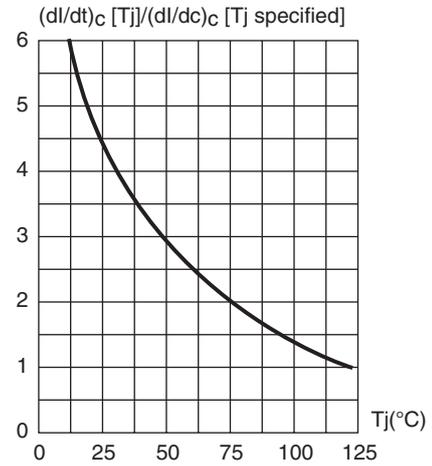
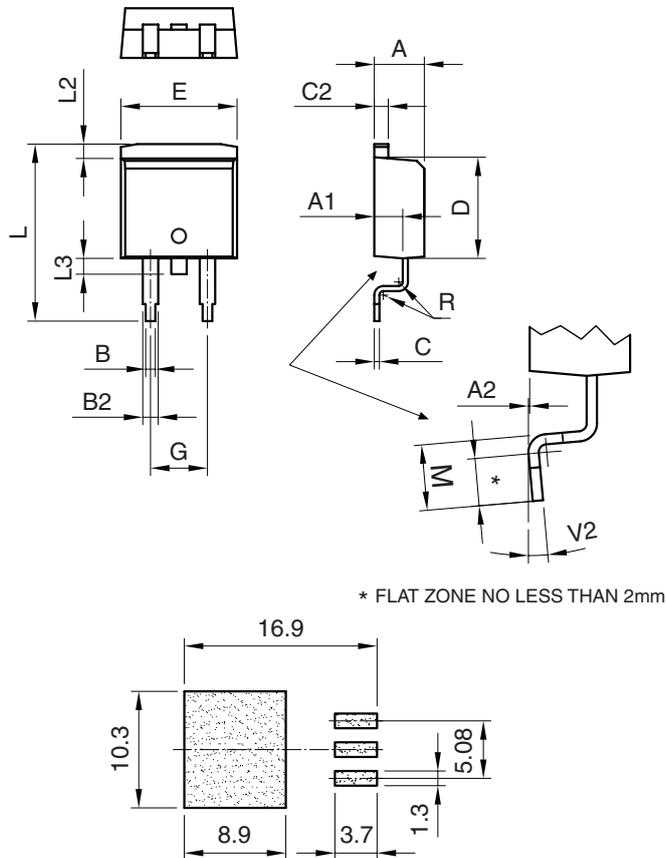


Fig. 8: Relative variation of critical rate of decrease of main current versus junction temperature



PACKAGE MECHANICAL DATA

D²PAK



REF.	DIMENSIONS		
	Milimeters		
	Min.	Nominal	Max.
A	4.40	4.45	4.60
A1	2.49	2.50	2.69
A2	0.03	0.10	0.23
B	0.70	0.90	0.93
B2	1.14	1.03	1.70
C	0.45	0.45	0.60
C2	1.23	1.23	1.36
D	8.95	9.00	9.35
E	10.00	10.25	10.40
G	4.88	5.15	5.28
L	15.00	15.40	15.85
L2	1.27	1.27	1.40
L3	1.40	1.55	1.75
M	2.40	3.00	3.20
R	0.40 typ		
V2	0°		8°

NOTE: LIMITING VALUES AND LIFE SUPPORT APPLICATIONS (SEE WEB PAGE).