



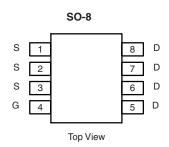
## N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
100	0.060 at V <sub>GS</sub> = 10 V	4.6	
	0.080 at V <sub>GS</sub> = 6 V	4.0	

### **FEATURES**

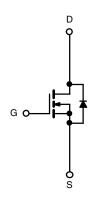
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4482DY-T1-E3 (Lead (Pb)-free)

Si4482DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unle	ss otherwise not	ted	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	I-	4.6	
	T <sub>A</sub> = 70 °C	I <sub>D</sub>	3.7	
Pulsed Drain Current		I <sub>DM</sub>	40	Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.1	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5	w
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	-D	1.6	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Maximum Junction-to-Ambient <sup>a</sup>	R <sub>thJA</sub>	50	°C/W		

Notes

a. Surface Mounted on FR4 board,  $t \le 10 \text{ s.}$ 

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Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static	L L		- V				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	μΑ	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			20		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 4.6 \text{ A}$		0.045	0.060	-	
		$V_{GS} = 6 \text{ V}, I_D = 4.0 \text{ A}$		0.050	0.080	Ω	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 4.6 \text{ A}$		20		S	
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	$I_S = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$			1.2	V	
Dynamic <sup>a</sup>				•			
Total Gate Charge	$Q_g$			30	50		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 4.6 \text{ A}$		7.5		nC	
Gate-Drain Charge	Q <sub>gd</sub>			7		1	
Gate Resistance	$R_g$		1		4.4	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	25		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 50 V, R <sub>L</sub> = 50 $\Omega$ I <sub>D</sub> $\cong$ 1 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 6 $\Omega$		12	25		
Turn-Off Delay Time	t <sub>d(off)</sub>			60	90	ns	
Fall Time	t <sub>f</sub>			25	40		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.1 A, dI/dt = 100 A/μs		50	80		

#### Notes:

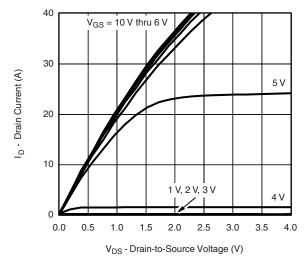
- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

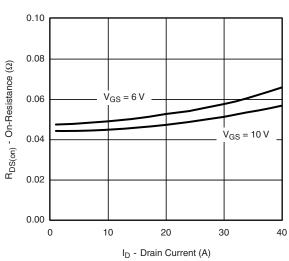




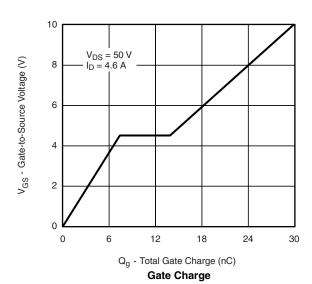
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

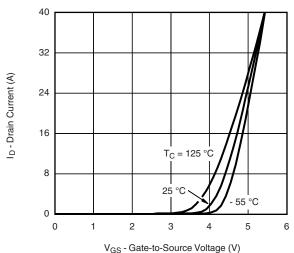


#### **Output Characteristics**

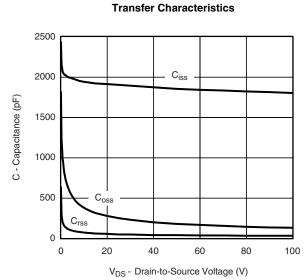


On-Resistance vs. Drain Current

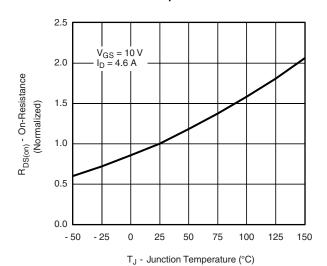




vas dato to course voltage (v)



Capacitance

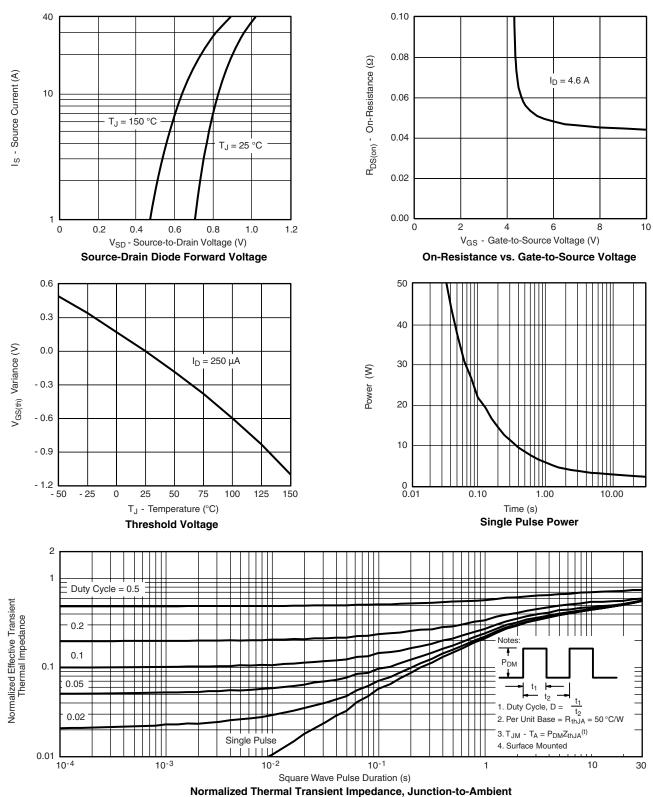


On-Resistance vs. Junction Temperature

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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