LITE-ON SEMICONDUCTOR

S8M15-B SERIES

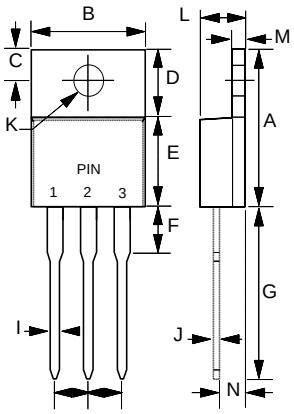
Sensitive Gate
Sillicon Controlled Rectifiers
Reverse Blocking Thyristors

SCRs
8 AMPERES RMS
400 thru 800 VOLTS

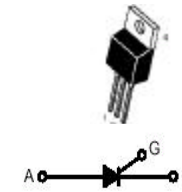
FEATURES

- Blocking Voltage of 400 thru 800 Volts
- On-State Current Rating of 8 Amperes RMS at 80℃
- High Surge Current Capability - 80 Amperes
- Rugged, Economical TO220AB Package
- Glass Passivated Junctions for Reliability and Uniformity
- Minimum and Maximum Values of IGT, VGT an IH Specified for Ease of Design
- High Immunity to dv/dt - 100 V/msec Minimum at 110℃
- Pb-Free Package

TO-220AB



TO-220AB		
DIM.	MIN.	MAX.
A	14.22	15.88
B	9.65	10.67
C	2.54	3.43
D	5.84	6.86
E	8.26	9.28
F	-	6.35
G	12.70	14.73
H	2.29	2.79
I	0.51	1.14
J	0.30	0.64
K	3.53 ⌀	4.09 ⌀
L	3.56	4.83
M	1.14	1.40
N	2.03	2.92
All Dimensions in millimeter		



PIN ASSIGNMENT	
1	Cathode
2	Anode
3	Gate
4	Anode

MAXIMUM RATINGS (Tj= 25℃ unless otherwise noticed)

Rating	Symbol	Value	Unit
Peak Repetitive Off– State Voltage (TJ= -40 to 125℃, Sine Wave, 50 to 60 Hz; Gate Open) S8M15400B S8M15600B S8M15800B	VDRM, VRRM	400 600 800	Volts
On-State RMS Current (180° Conduction Angles, TC = 80℃)	IT(RMS)	8	Amp
Peak Non-Repetitive Surge Current (1/2 Cycle, SIne Wave 60 Hz, Tj = 25℃)	ITSM	80	Amp
Circuit Fusing Consideration (t = 8.3 ms)	I²t	26.5	A²s
Forward Peak Gate Power (Pulse Width ≦ 1.0 us, Tc = 80℃)	PGM	5.0	Watt
Forward Average Gate Power (t= 8.3 ms, Tc = 80℃)	PG(AV)	0.5	Watt
Forward Peak Gate Current (Pulse Width ≦ 1.0 us, Tc = 80℃)	IGM	2.0	Amp
Operating Junction Temperature Range	TJ	-40 to +125	℃
Storage Temperature Range	Tstg	-40 to +150	℃

Notice: (1) VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded

REV. 3, Jun-2005, KTXC11

THERMAL CHARACTERISTICS

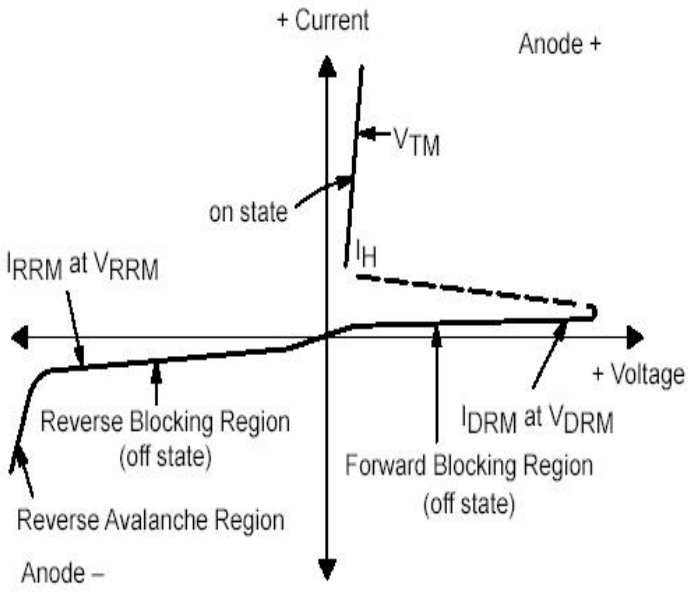
Characteristic	Symbol	Value	Unit
Thermal Resistance - Junction to Case - Junction to Ambient	RthJC RthJA	2.2 62.5	℃/W
Maximum Lead Temperature for Soldering Purposes 1/8 from Case for 10 Seconds	T_L	260	℃

ELECTRICAL CHARACTERISTICS (T_J=25 ℃ unless otherwise noted)

Characteristics	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Peak Reptitive Forward or Reverse Blocking Current T_J=25℃ (V_D=Rated V_{DRM} and V_{RRM}; Gate Open) T_J=125℃	I_{DRM} I_{RRM}	---- ----	---- ----	10 2.0	uA mA
ON CHARACTERISTICS					
Peak Forward On-State Voltage (I_{TM}= 16A Peak @Tp ≦2.0 ms, Duty Cycle ≦ 2%)	V_{TM}	----	----	1.8	Volts
Gate Trigger Current (Continuous dc) (V_D = 12 V; R_L = 100 Ohms)	I_{GT}	2.0	7.0	15	mA
Holding Current (V_D = 12 V, Gate Open, Initiating Current = 200 mA)	I_H	4.0	17	30	mA
Latch Current (V_D = 12 V, I_G = 15 mA)	I_L	6.0	20	40	mA
Gate Trigger Voltage (Continuous dc) (V_D = 12 V; R_L =100 Ohms)	V_{GT}	0.5	0.65	1.0	Volts
Gate Non - Trigger Voltage T_J=125℃ (V_D = 12V, RL =100 Ohms)	V_{GD}	0.2	---	---	Volts
DYNAMIC CHARACTERISTICS					
Critical Rate of Rise of Off-State Voltage (V_D=Rated V_{DRM},Exponential Waveform, Gate Open,T_J=125℃)	dv/dt	100	250	----	V/us
Repetitive Critical Rate of Rise of On-State Current IPK=50A,Pw=40 usec,diG/dt=1A/usec,Igt=50mA	di/dt	----	----	50	A/us

*Indicates Pulse Test: Pulse Width ≦ 2.0 ms, Duty Cycle ≦ 2%.

Symbol	Parameter
V_{DRM}	Peak Repetitive Off State Forward Voltage
I_{DRM}	Peak Forward Blocking Current
V_{RRM}	Peak Repetitive Off State Reverse Voltage
I_{RRM}	Peak Reverse Blocking Current
V_{TM}	Peak On State Voltage
I_H	Holding Current



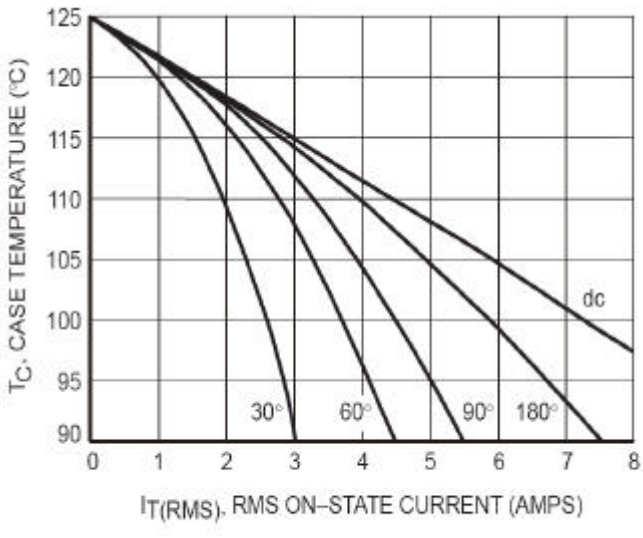


Figure 1. Typical RMS Current Derating

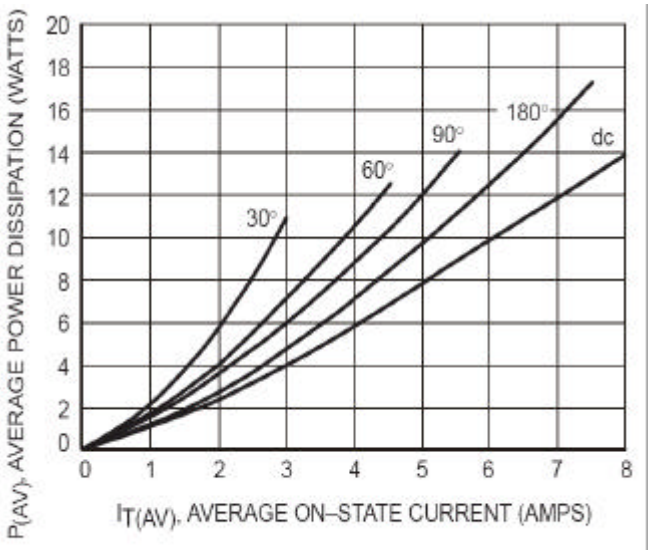


Figure 2. On–State Power Dissipation

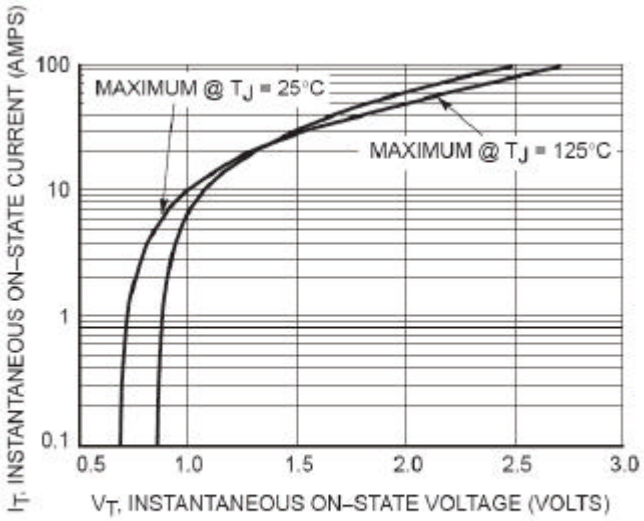


Figure 3. Typical On–State Characteristics

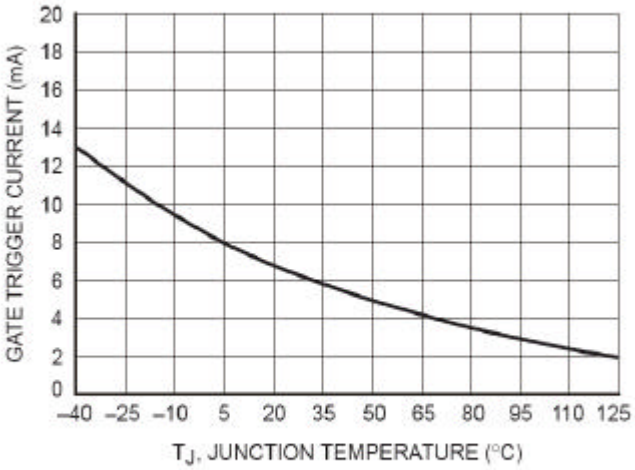


Figure 4. Typical Gate Trigger Current versus Junction Temperature

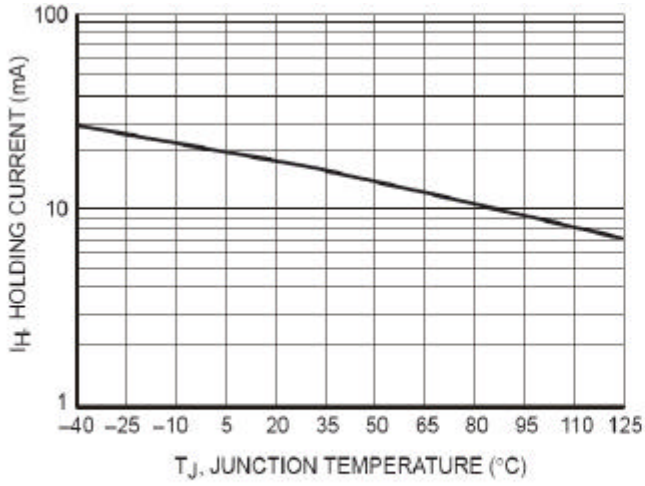


Figure 5. Typical Holding Current versus Junction Temperature

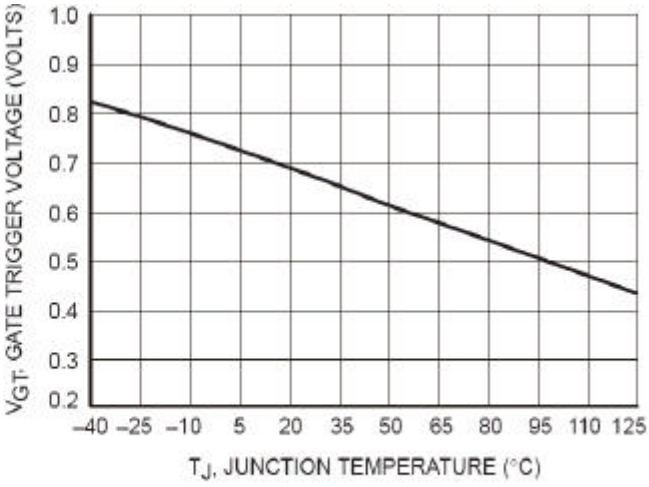


Figure 6. Typical Gate Trigger Voltage versus Junction Temperature

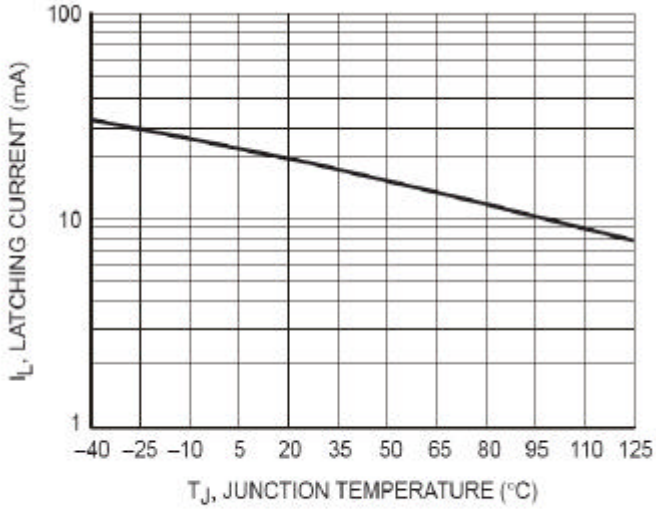


Figure 7. Typical Latching Current versus Junction Temperature