



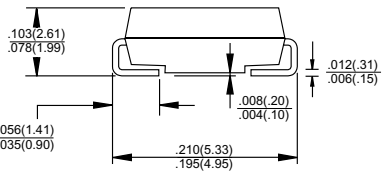
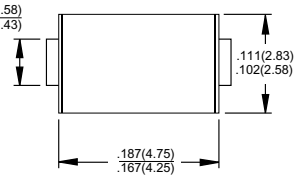
SK24A THRU SK26A

2.0 AMPS. Surface Mount Schottky Barrier Rectifiers



Voltage Range
40 to 60Volts
Current
2.0 Amperes

SMA/DO-214AC



Dimensions in inches and (millimeters)

Features

- ✧ For surface mounted application
- ✧ Metal to silicon rectifier, majority carrier conduction
- ✧ Low forward voltage drop
- ✧ Easy pick and place
- ✧ High surge current capability
- ✧ Plastic material used carriers Underwriters Laboratory Classification 94V-0
- ✧ Epitaxial construction
- ✧ High temperature soldering: 250°C / 10 seconds at terminals

Mechanical Data

- ✧ Case: Molded plastic
- ✧ Terminals: Solder plated
- ✧ Polarity: Indicated by cathode band
- ✧ Packaging: 12mm tape per EIA STD RS-481
- ✧ Weight: 0.093gram

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	SK24A	SK25A	SK26A	Units
Maximum Recurrent Peak Reverse Voltage	40	50	60	V
Maximum RMS Voltage	28	35	42	V
Maximum DC Blocking Voltage	40	50	60	V
Maximum Average Forward Rectified Current at T _L (See Fig. 1)	2.0			A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	50			A
Maximum Instantaneous Forward Voltage (Note 1) @ 2.0A	0.5	0.7		V
Maximum DC Reverse Current @ T _A = 25°C at Rated DC Blocking Voltage	0.5			mA mA
Typical Thermal Resistance (Note 2) R θ JA	88			°C/W
Operating Temperature Range T _J	-65 to +125	-65 to +150		°C
Storage Temperature Range T _{STG}	-65 to +150			°C

Notes: 1. Pulse Test with PW=300 usec, 1% Duty Cycle

2. Measured on P.C.Board with 0.2 x 0.2" (5.0 x 5.0mm) Copper Pad Areas.

RATINGS AND CHARACTERISTIC CURVES (SK24A THRU SK26A)

FIG. 1- MAXIMUM FORWARD CURRENT DERATING CURVE

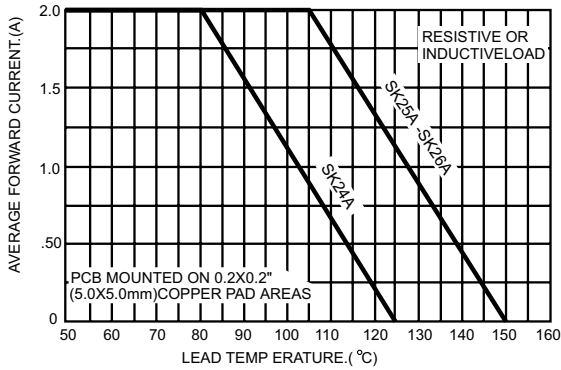


FIG. 2- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

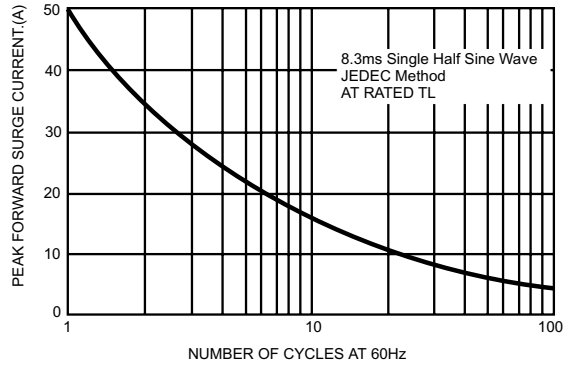


FIG. 3- TYPICAL FORWARD CHARACTERISTICS

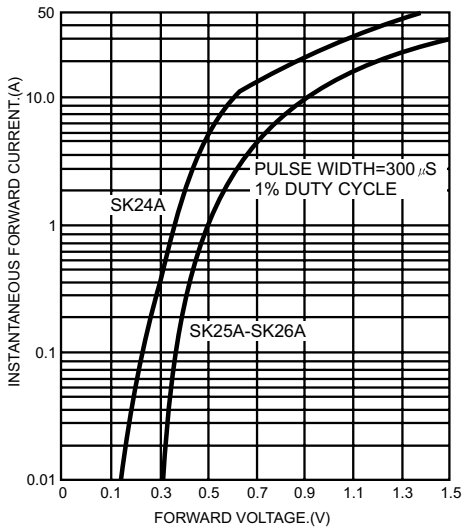


FIG. 4- TYPICAL REVERSE CHARACTERISTICS

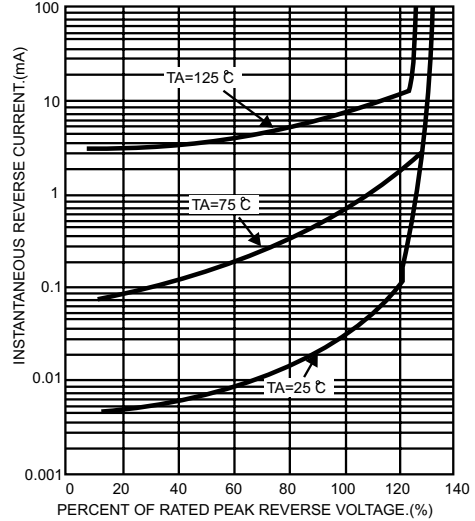


FIG. 5- TYPICAL JUNCTION CAPACITANCE

