

DISPLAY Elektronik GmbH

DATA SHEET

LCD MODULE

DEM 16218 SGH

Product Specification

Version : 1

21/Oct/2008

GENERAL SPECIFICATION

MODULE NO:

DEM 16218 SGH

CUSTOMER P/N

VERSION NO.	CHANGE DESCRIPTION	DATE
0	ORIGINAL VERSION	07.04.2003
1	CHANGE LCD-DRIVER	21.10.2008

PREPARED BY: CXG

DATE: 21.10.2008

APPROVED BY: MH

DATE: 21.10.2008

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1. FUNCTIONS & FEATURES

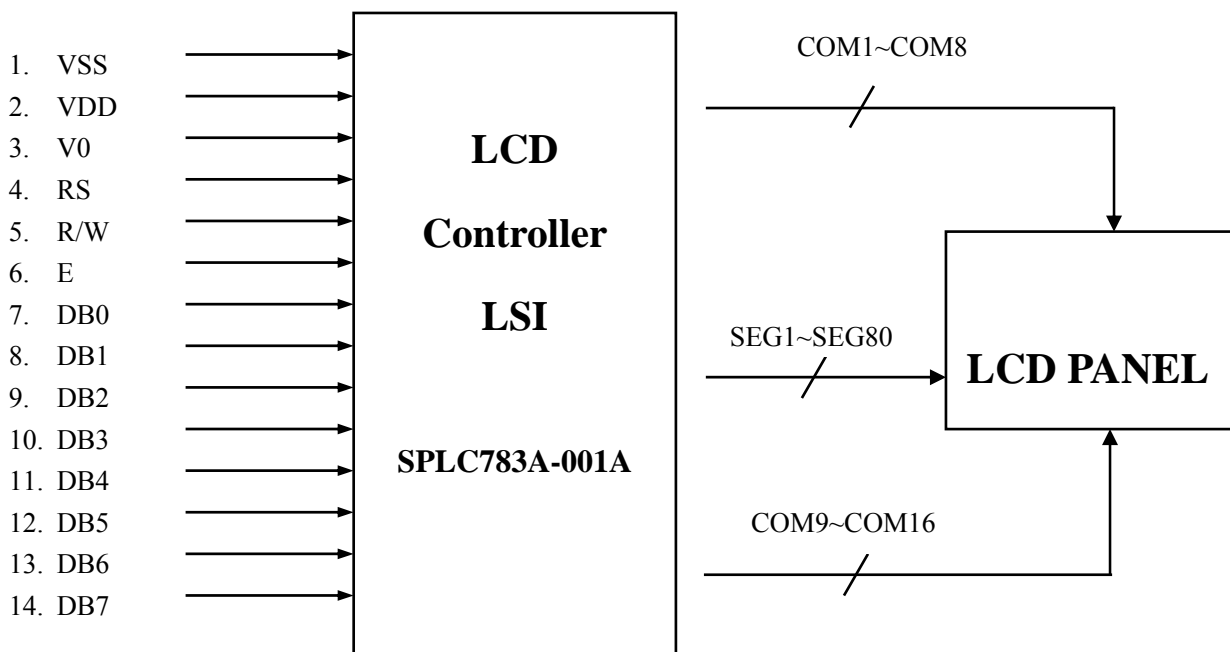
MODULE NAME	LCD TYPE
DEM 16218 SGH	STN Grey Reflective Positive Mode

- Viewing Direction : 6 o'clock
- Driving Scheme : 1/16 Duty Cycle, 1/5 Bias
- Power Supply Voltage : 5 Volt (typ.)
- VLCD Adjustable For Best Contrast : 4.5 Volt (typ.)
- Display contents : 16 x 2 Characters
- Operation temperature : -20°C to +70°C
- Storage temperature : -25°C to +75°C
- Internal Memory : CGROM (8,320 bits)
: CGRAM (64 x 8 bits)
: DDRAM (80 x 8 bits)
- CGROM : CGROM of the SPLC783A-001A (Sunplus)
- Interface : Easy Interface with a 4-bit or 8-bit MPU

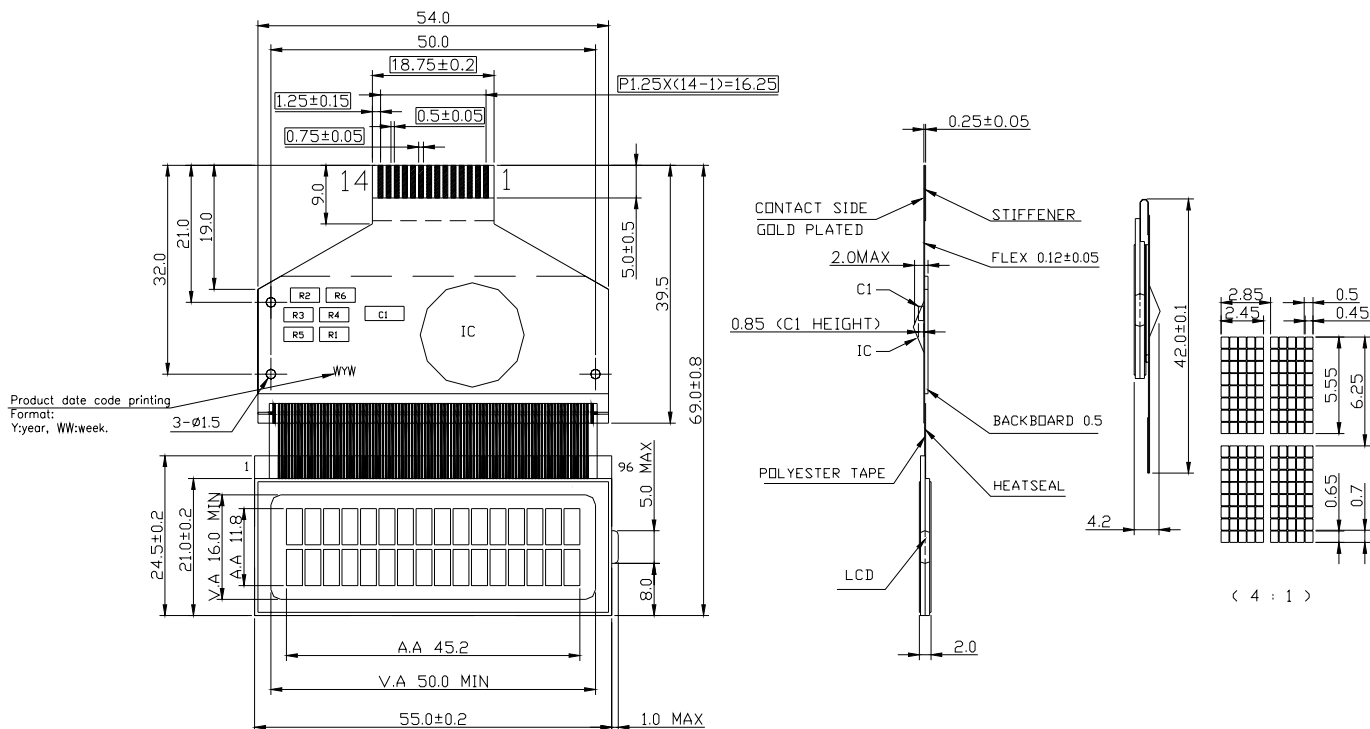
2. MECHANICAL SPECIFICATIONS

- Character Pitch : 2.85 x 6.25 mm
- Character Size : 2.45 x 5.55 mm
- Character Font : 5 x 8 dots
- Dot Size : 0.45 x 0.65 mm
- Dot Pitch : 0.50 x 0.70 mm

3. BLOCK DIAGRAM



4. EXTERNAL DIMENSIONS

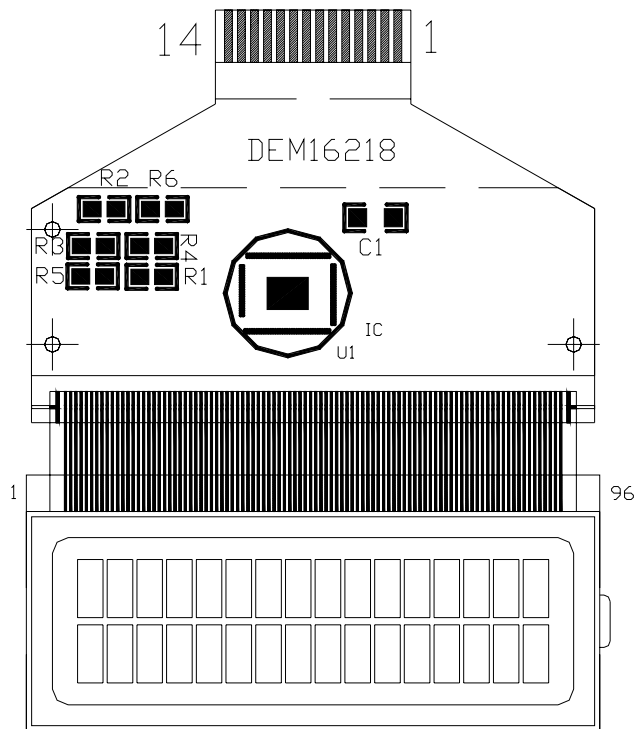


Remarks:
 1.Unmarked tolerance is ±0.3;
 2.All material comply with RoHs.

5. PIN ASSIGNMENT

Pin No.	Symbol	Function
1	VSS	Ground terminal of module.
2	VDD	Power terminal of module 2.7V to 5.5V.
3	V0	Power Supply for liquid crystal drive.
4	RS	Register select RS = 0... Instruction register RS = 1... Data register
5	R/W	Read /Write R/W = 1... Read R/W = 0... Write
6	E	Read/Write Enable Signal
7	DB0	Bi-directional data bus, data transfer is performed once, thru DB0 to DB7, in the case of interface data. Length is 8-bits; and twice, thru DB4 to DB7 in the case of interface data length is 4-bits. Upper four bits first then lower four bits.
8	DB1	
9	DB2	
10	DB3	
11	DB4	
12	DB5	
13	DB6	
14	DB7	

6. THE MODULE NO. IS PRINTED ON THE PCB



Note:UL mark is printed on the FPC

7. DISPLAY DATA RAM (DDRAM)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	← DISPLAY POSITION
FIRST LINE	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	← DDRAM ADDRESS
SECOND LINE	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	

8. MAXIMUM ABSOLUTE POWER RATINGS (Ta=25°C)

Item	Symbol	Standard value	Unit
Power supply voltage(1)	V _{DD}	-0.3~+7.0	V
Power supply voltage(2)	V ₀	V _{DD} -15.0~V _{DD} +0.3	V
Input voltage	V _{IN}	-0.3~V _{DD} +0.3	V
Operating temperature	T _{opr}	-20~+70	°C
Storage temperature	T _{stg}	-25~+75	°C

*Voltage greater than above may damage to the Circuit.

$$V_{DD} > V_1 > V_2 > V_3 > V_4 > V_5$$

9. ELECTRICAL CHARACTERISTICS

9.1 DC Characteristics ($V_{DD} = 4.5V$ to $5.5V$, $T_a = 25^{\circ}C$)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
Input High Voltage	V_{IH1}	2.2	-	V_{DD}	V	Pins:(E, RS, R/W, DB0 - DB7)
Input Low Voltage	V_{IL1}	-0.3	-	0.6	V	
Input High Voltage	V_{IH2}	$V_{DD}-1$	-	V_{DD}	V	Pin OSC1
Input Low Voltage	V_{IL2}	-0.2	-	1.0	V	Pin OSC1
Output High Voltage (TTL)	V_{OH1}	2.4	-	V_{DD}	V	$I_{OH} = -0.205mA$ Pins: DB0 - DB7
Output Low Voltage (TTL)	V_{OL1}	-	-	0.4	V	$I_{OL} = 1.2mA$ Pins: DB0 - DB7
Output High Voltage (CMOS)	V_{OH2}	$0.9V_{DD}$	-	V_{DD}	V	$I_{OH} = -40\mu A$, Pins: CLK1, CLK2, M, D
Output Low Voltage (CMOS)	V_{OL2}	-	-	$0.1V_{DD}$	V	$I_{OL} = 40\mu A$, Pins: CLK1, CLK2, M, D
Driver ON Resistance (COM)	R_{COM}	-	-	10k	k Ω	$I_O = \pm 50\mu A$, $V_{LCD} = 4.0V$ Pins: COM1 - COM16
Driver ON Resistance (SEG)	R_{SEG}	-	-	15k	k Ω	$I_O = \pm 50\mu A$, $V_{LCD} = 4.0V$ Pins: SEG1 - SEG80
LCD Voltage	V_{LCD}	3.0	-	11	V	$V_{DD}-V_5$, 1/4 bias or 1/5 bias

9-2 AC Characteristics ($V_{DD} = 4.5$ to $5.5V$, $T_a = 25^{\circ}C$)

9-2-1 Internal clock operation

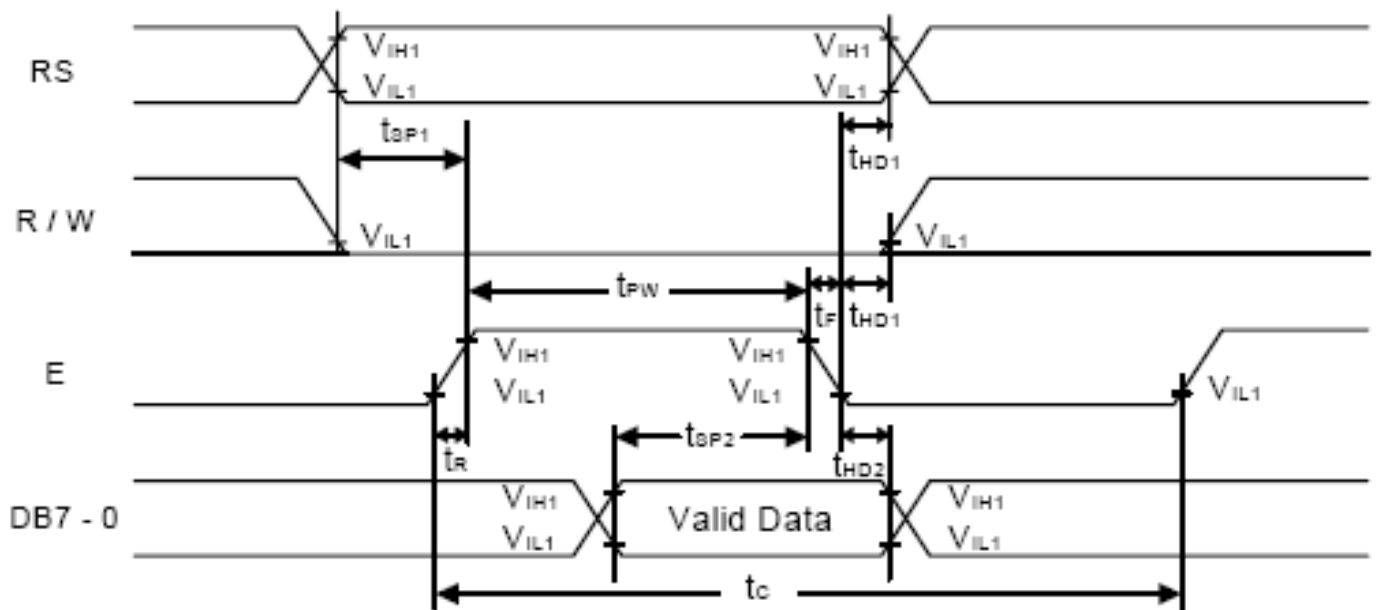
Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
OSC Frequency	F_{OSC1}	190	270	350	kHz	$V_{DD} = 5.0V$, $R_f = 91K\Omega \pm 2\%$

9-2-2 External clock operation

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
External Frequency	F_{OSC2}	125	250	350	kHz	
Duty Cycle		45	50	55	%	
Rise/Fall Time	t_r, t_f	-	-	0.2	μs	

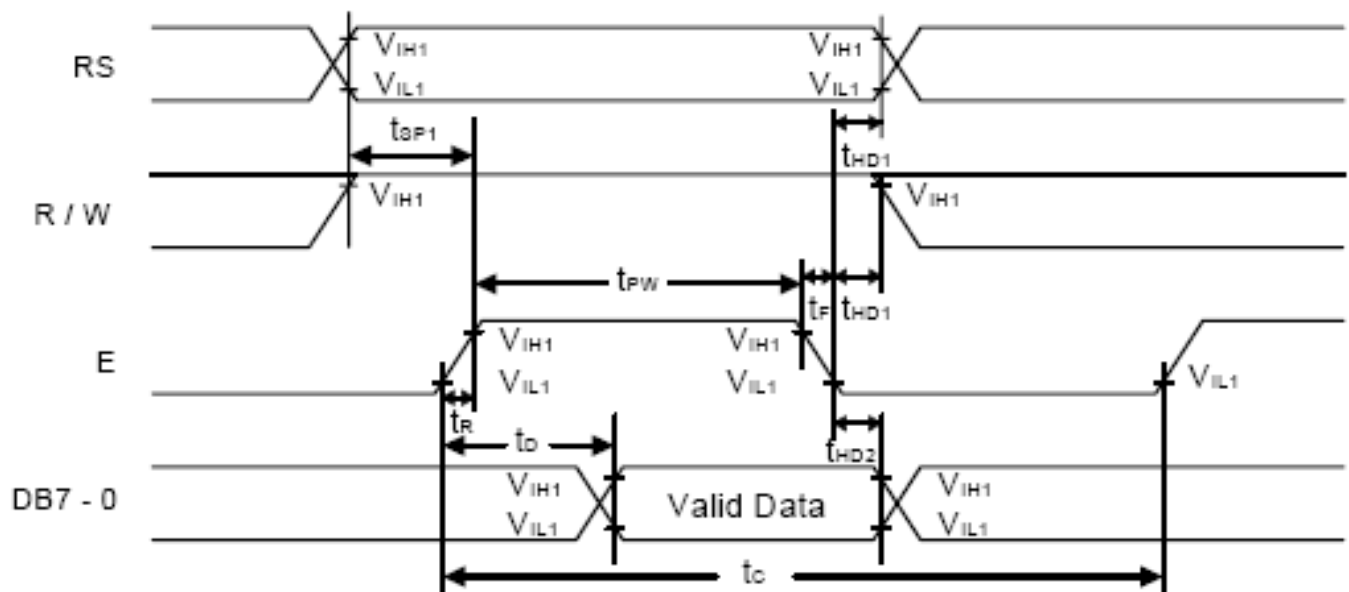
9-2-3 Write mode (writing data from MPU to SPLC783A)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
E Cycle Time	t_c	500	-	-	ns	Pin E
E Pulse Width	t_{PW}	220	-	-	ns	Pin E
E Rise/Fall Time	t_R, t_F	-	-	25	ns	Pin E
Address Setup Time	t_{SP1}	40	-	-	ns	Pins: RS, R/W, E
Address Hold Time	t_{HD1}	10	-	-	ns	Pins: RS, R/W, E
Data Setup Time	t_{SP2}	60	-	-	ns	Pins: DB0 - DB7
Data Hold Time	t_{HD2}	10	-	-	ns	Pins: DB0 - DB7



9-2-4 Read mode (Reading Data from SPLC783A to MPU)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
E Cycle Time	t_C	500	-	-	ns	Pin E
E Pulse Width	t_{PW}	220	-	-	ns	Pin E
E Rise/Fall Time	t_R, t_F	-	-	25	ns	Pin E
Address Setup Time	t_{SP1}	40	-	-	ns	Pins: RS, R/W, E
Address Hold Time	t_{HD1}	10	-	-	ns	Pins: RS, R/W, E
Data Output Delay Time	t_D	-	-	120	ns	Pins: DB0 - DB7
Data hold time	t_{HD2}	20	-	-	ns	Pin DB0 - DB7



10. CONTROL AND DISPLAY COMMAND

Instruction	Instruction Code										Description	Execution Time ($f_{osc}=270Khz$)	
	R S	R/W	DB 7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC	1.52ms	
Return home	0	0	0	0	0	0	0	0	0	1	*	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry mode set	0	0	0	0	0	0	0	0	1	I/D	S	Assign cursor moving direction and enable	38us
Display ON/OFF control	0	0	0	0	0	0	0	1	D	C	B	Set display/cursor/blink of cursor on/off control bit	38us
Cursor or Display shift	0	0	0	0	0	0	1	S/C	R/L	*	*	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	38us
Function set	0	0	0	0	1	DL	N	F	*	*	*	Set interface data length (DL: 8-bit/ 4-bit), numbers of display line(N: 2-line/1-line) and display font type (F: 5x10 dots/5x8 dots)	38us
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0		Set CGRAM address in address counter.	38us
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0		Set DDRAM address in address counter.	38us
Read busy flag and address counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0		Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	
Write data	1	0	D7	D6	D5	D4	D3	D2	D1	D0		Write data into internal RAM (DDRAM / CGRAM /).	38us
Read data	1	1	D7	D6	D5	D4	D3	D2	D1	D0		Read data from internal RAM (DDRAM / CGRAM /).	38us

Note: “”: don’t care.

11. STANDARD CHARACTER PATTERN (SPLC783A-001A)

Upper(上) Lower(下)		LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
		LLLL				0	1	2	3	4	5	6	7	8	9	A	B
LLLH			!	!	!	!	!	!	!	!	!	!	!	!	!	!	!
LLH			"	2	B	R	b	r			7	7	7	7	7	7	7
LLHH			#	3	C	S	c	s			J	7	7	7	7	7	7
LHLL			*	4	D	T	d	t			7	7	7	7	7	7	7
LHLH			%	5	E	U	e	u			*	*	*	*	*	*	*
LHHL			&	6	F	V	f	v			7	7	7	7	7	7	7
LHHH			'	7	G	W	g	w			7	7	7	7	7	7	7
HLLL			(8	H	X	h	x			7	7	7	7	7	7	7
HLLH)	9	I	Y	i	y			7	7	7	7	7	7	7
HLHL			*	!	J	Z	j	z			7	7	7	7	7	7	7
HLHH			+	!	K	L	k	l			7	7	7	7	7	7	7
HHLL			,	<	L	*	l	l			7	7	7	7	7	7	7
HHLH			-	=	N	I	n	i			7	7	7	7	7	7	7
HHHL			.	>	N	^	n	^			7	7	7	7	7	7	7
HHHH			/	?	O	_	o	_			7	7	7	7	7	7	7

12. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD module.
 - Tools required for assembly, such as soldering irons, must be properly grounded.
 - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- Storage precautions
When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

13. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules :
 - Exposed area of the printed circuit board
 - Terminal electrode sections